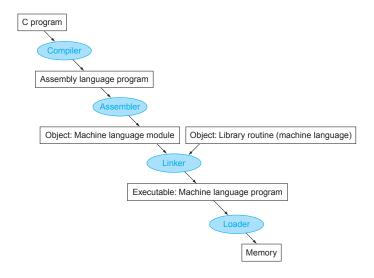
CSE 305: Computer Architecture

Tanvir Ahmed Khan takhandipu@gmail.com

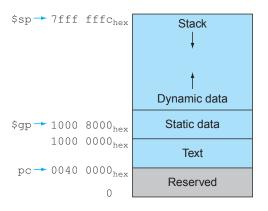
Department of Computer Science and Engineering Bangladesh University of Engineering and Technology.

October 14, 2015

C Code Translation & Startup

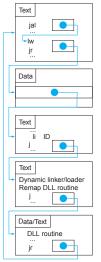


MIPS Memory Allocation for Program & Data

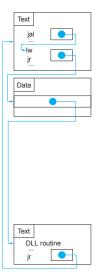


Dynamic Linking

DLLs

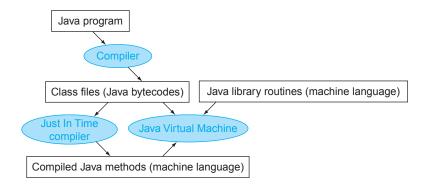


(a) First call to DLL routine



(b) Subsequent calls to DLL routine

Starting Java Applications



Fallacies

- ▶ Powerful instruction ! = higher performance
- ▶ Use Assembly Code ! = higher performance
- ▶ Backward compatibility ! = instruction set doesn't change

Concluding Remarks

- 1. Simplicity favors regularity
- 2. Smaller is faster
- 3. With Good design comes good compromises
- 4. Make the common case fast

Reference

- ► Computer Organization and Design: The Hardware/Software Interface, *Chapter 2, 2.11, 2.12, 2.19, 2.20*
 - ▶ David A. Patterson
 - ▶ John L. Hennessy

Class Test 2 Syllabus: Chapter 2

